

The schematic diagram of the semiconductor device 900 illustrates its internal architecture. It features two primary functional blocks, 930 and 932, which are interconnected via a central bus 934. Block 930, located in the upper portion, contains a series of vertical rectangular components labeled 901, 903, and 905, and a row of horizontal rectangular components labeled 907 and 909. These components are connected to a common ground line 945. Block 932, located in the lower portion, contains a series of vertical rectangular components labeled 911, 913, and 915, and a row of horizontal rectangular components labeled 921, 923, 925, and 927. These components are also connected to a common ground line 929. A signal line 924 enters block 930 from the right, and a signal line 926 exits block 932 to the right. A bidirectional signal line 928 is connected to the bus 934 on the left side of the device.

FIG. 1
(Prior Art)

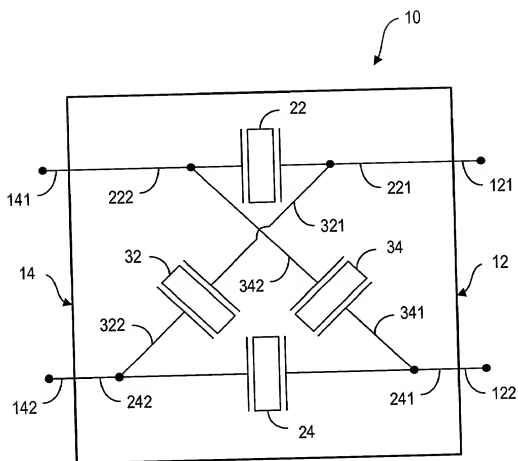


FIG. 2

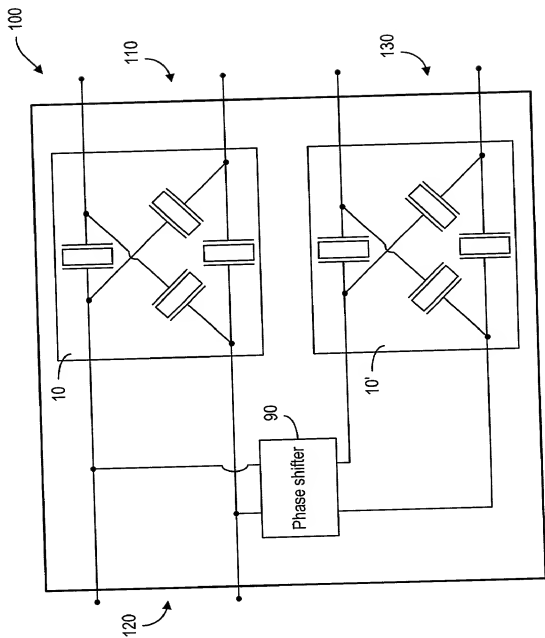


FIG. 3

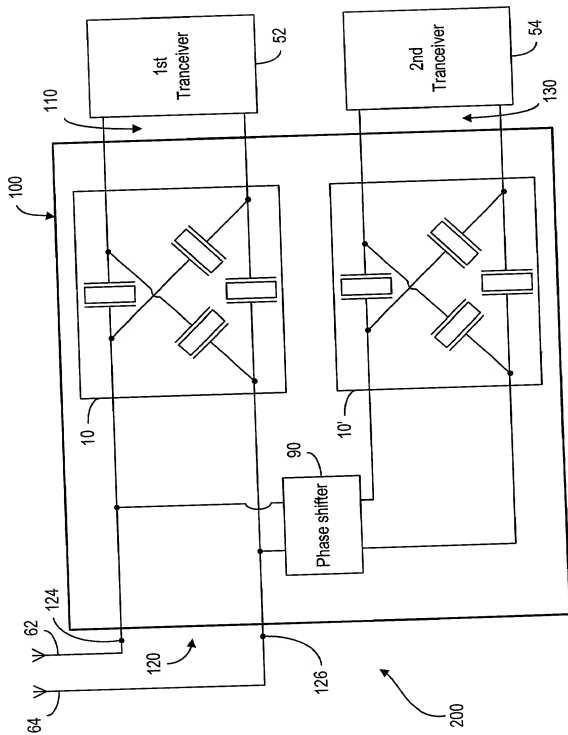


FIG. 4

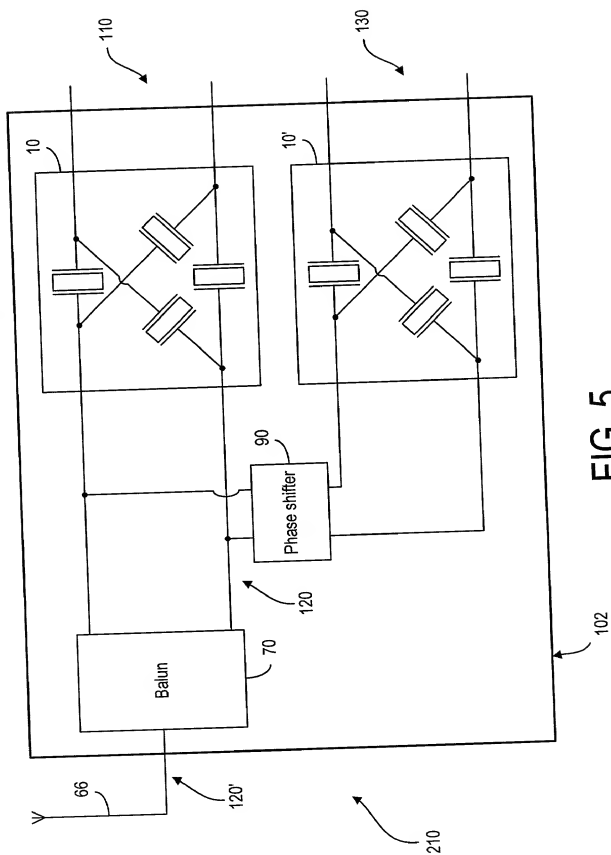


FIG. 5

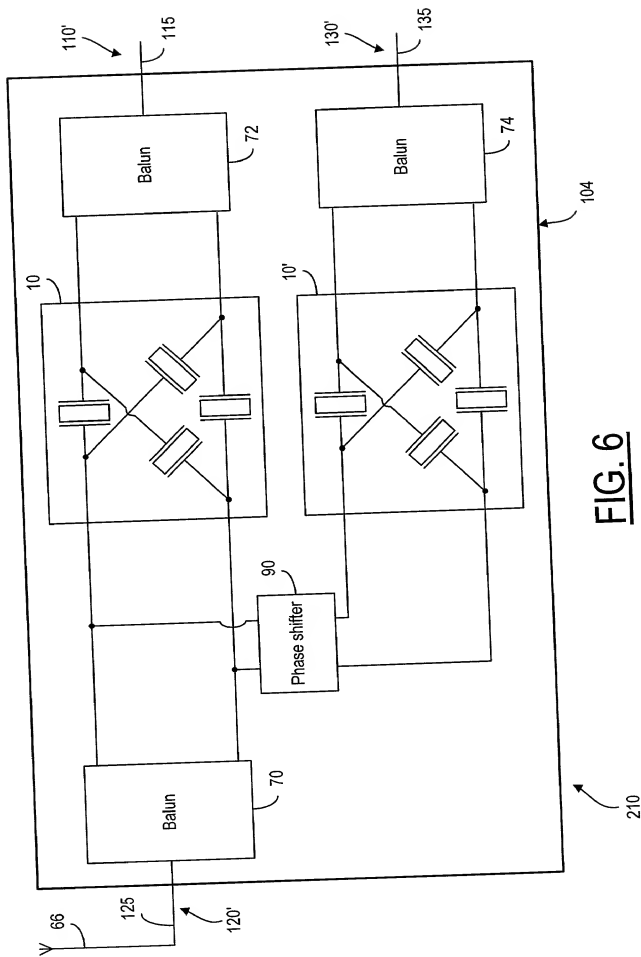


FIG. 6